

FIG. 2

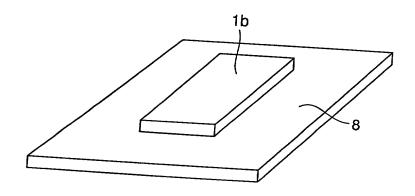


FIG. 3

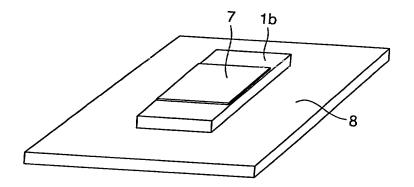


FIG. 4

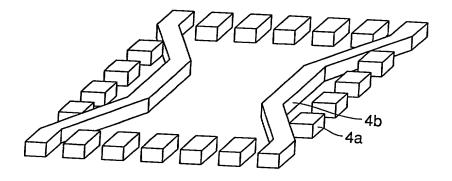


FIG. 5

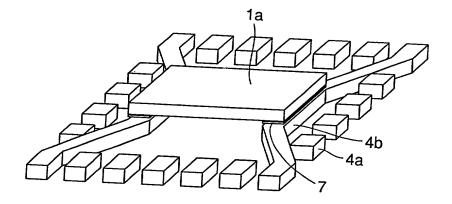


FIG. 6

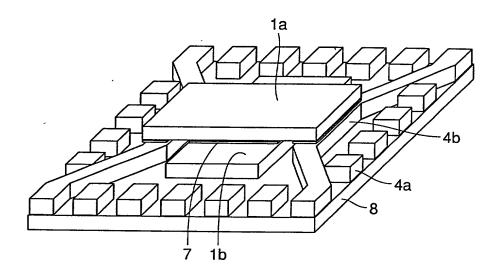


FIG. 7

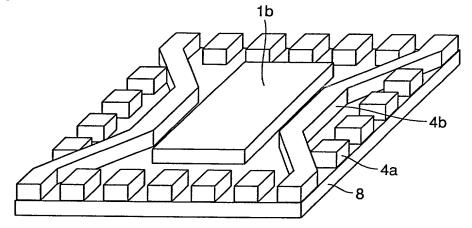


FIG. 8

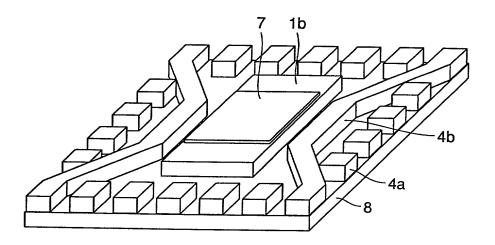


FIG. 9

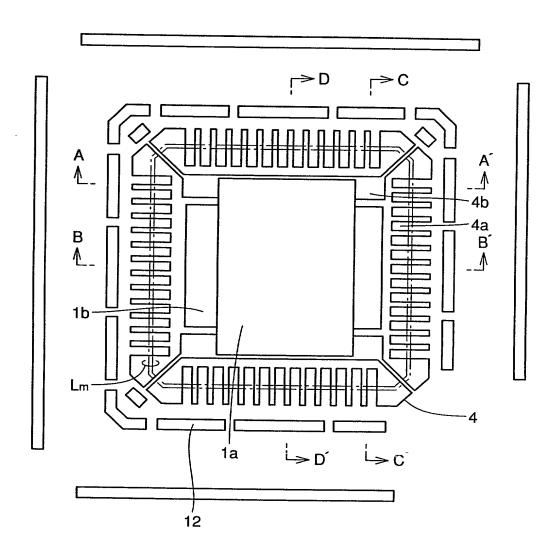


FIG. 10

<u>A-A'</u>

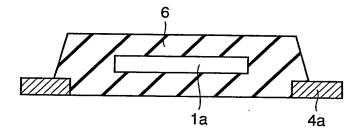


FIG. 11

B-B'

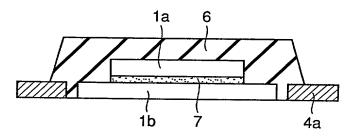


FIG. 12

<u>C-C'</u>

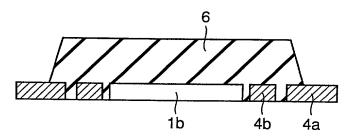


FIG. 13

<u>D-D'</u>

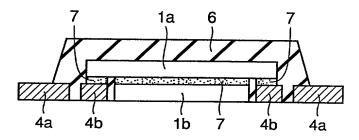


FIG. 14

<u>A-A'</u>

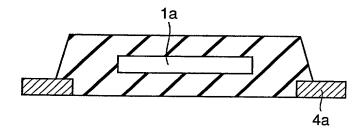


FIG. 15

<u>B-B'</u>

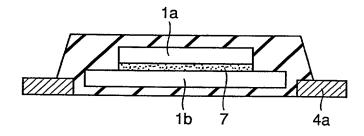


FIG. 16

<u>C-C'</u>

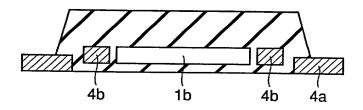


FIG. 17

D-D'

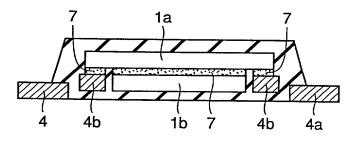


FIG. 18

<u>A-A'</u>

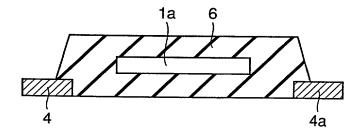


FIG. 19

B-B'

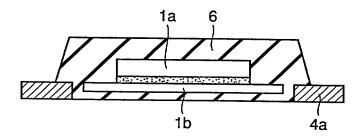


FIG. 20

<u>C-C</u>'

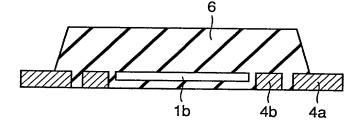
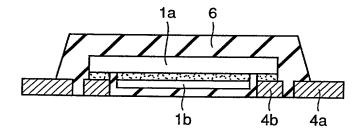
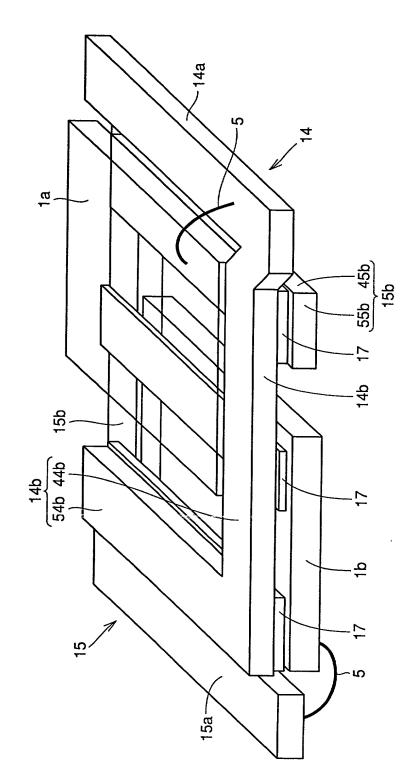


FIG. 21

D-D'





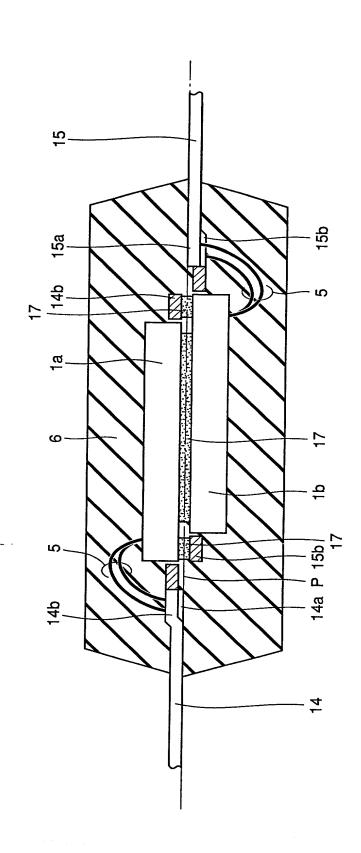


FIG. 24

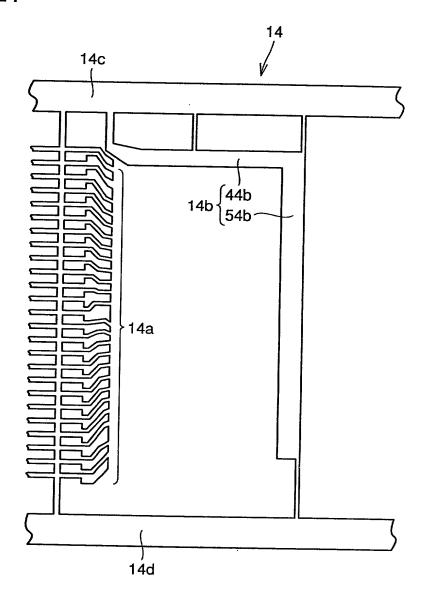


FIG. 25

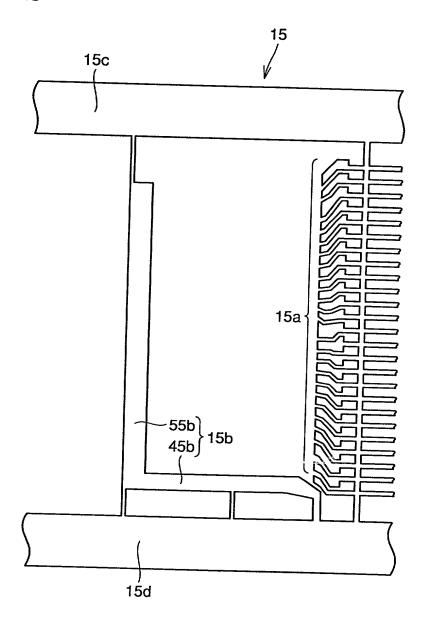
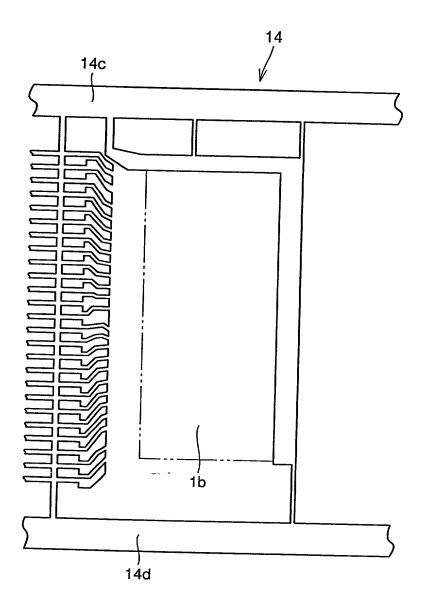
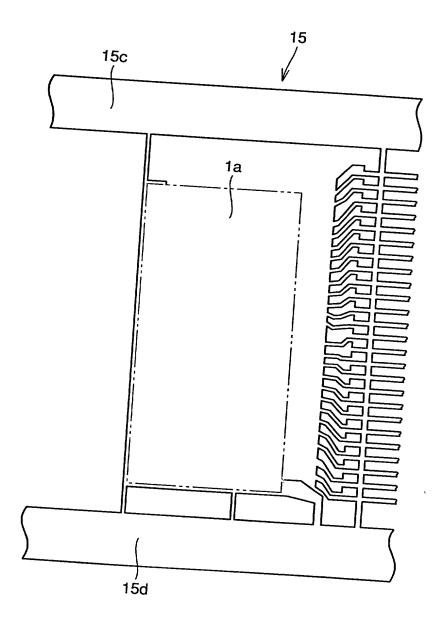


FIG. 26



Title: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF Inventors: ABE ET AL.
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202-737-6770

FIG. 27



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FIG. 28

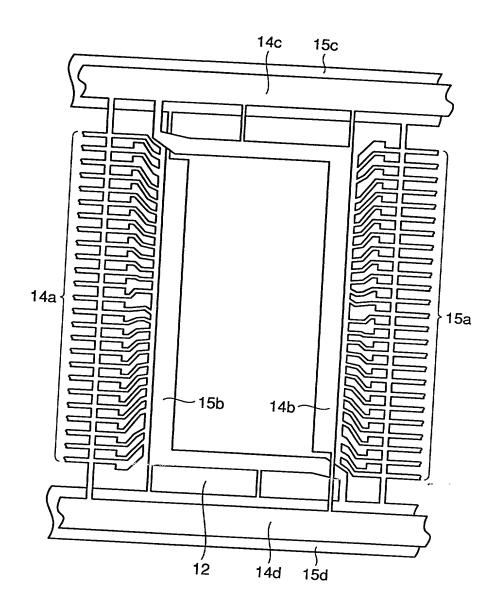
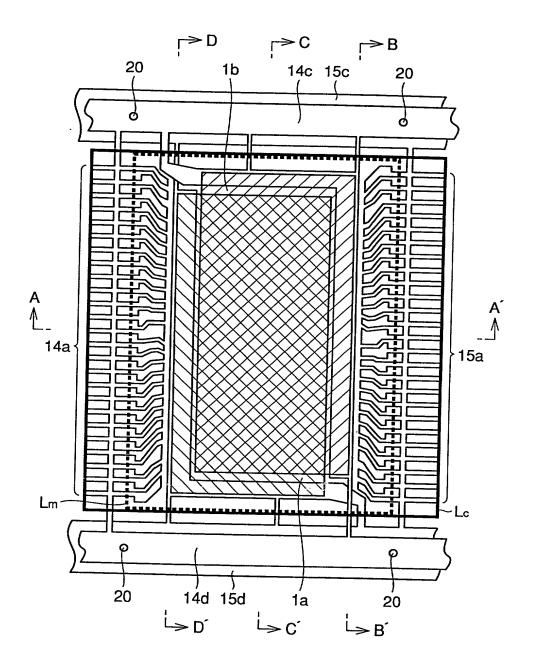
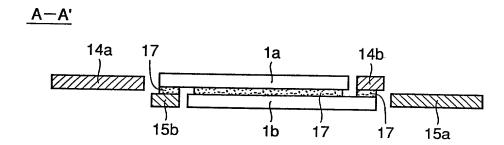


FIG. 29

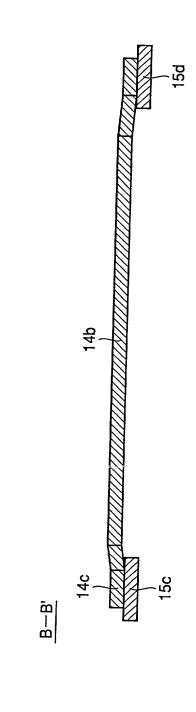


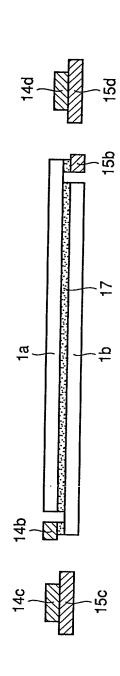
Title: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

FIG. 30



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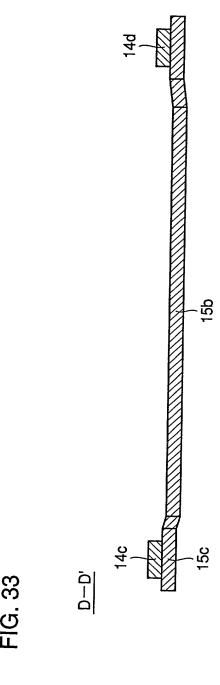




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Atty Docket No.: 401263
Leydig, Voit & Mayer, Ltd.
202-737-6770

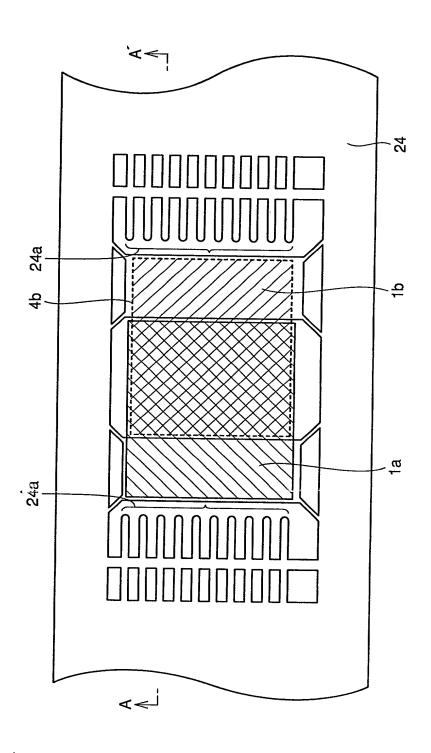


FIG. 35



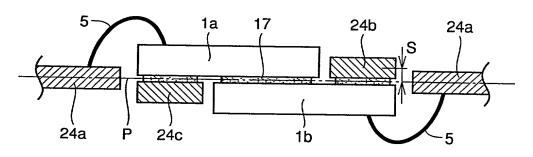


FIG. 36 PRIOR ART

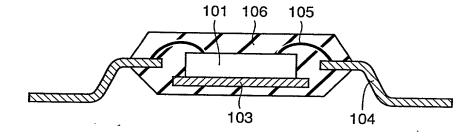


FIG. 37 PRIOR ART

